

SYSTEM AND METHOD FOR ANALYZING POWER DISTRIBUTION USING STATIC TIMING ANALYSIS

ABSTRACT

A method and system for analyzing power distribution in an integrated circuit chip
5 includes dividing a clock cycle of the integrated circuit chip into a plurality of time periods,
dividing the integrated circuit chip into a plurality of cells, performing a static timing analysis for
the plurality of cells to obtain current waveform data for each cell and each time period, and
performing a power distribution analysis using the current waveform data.

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